

| L Number | Hits  | Search Text                                                                                 | DB                                          | Time stamp       |
|----------|-------|---------------------------------------------------------------------------------------------|---------------------------------------------|------------------|
| -        | 0     | (semiconductor) and (secuity) and (control adj signal\$3)                                   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 13:29 |
| -        | 12117 | (semiconductor) and (secur\$3) and (control adj signal\$3)                                  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 13:30 |
| -        | 139   | ((semiconductor) and (secur\$3)).ti.                                                        | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 13:31 |
| -        | 9     | ((semiconductor) and (secur\$3)).ti. ) and (control adj signal\$3)                          | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 15:55 |
| -        | 27999 | (ICE\$2) and (secur\$3)                                                                     | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 15:59 |
| -        | 1258  | (ICE\$2) and (secur\$3) and (control adj signal\$3)                                         | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 15:57 |
| -        | 79    | (ICE\$2) and (secur\$3) and (control adj signal\$3) and (supervisor\$3)                     | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 15:58 |
| -        | 101   | (in\$1circuit adj emulator\$2) and (secur\$3)                                               | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 16:02 |
| -        | 40    | ((in\$1circuit adj emulator\$2) and (secur\$3)) and (control adj signal\$3)                 | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 16:00 |
| -        | 58    | (in\$1circuit adj emulator\$2) and (secur\$2)                                               | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 16:33 |
| -        | 5     | (in\$1circuit adj emulator\$2) and (secur\$2 adj mode\$2)                                   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 16:32 |
| -        | 20    | ((in\$1circuit adj emulator\$2) and (secur\$2)) and (control adj (circuit\$2 or signal\$2)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/25 16:34 |
| -        | 56533 | (command\$2) same ((software interrupt\$2) or (SWI))                                        | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:27 |
| -        | 8812  | in\$1circuit emulator\$2                                                                    | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:27 |
| -        | 1739  | ((command\$2) same ((software interrupt\$2) or (SWI)) ) and (in\$1circuit emulator\$2)      | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:27 |
| -        | 30777 | (command\$2) with ((software interrupt\$2) or (SWI))                                        | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:31 |
| -        | 2495  | (command\$2) with (generat\$3 near3 ((software interrupt\$2) or (SWI)))                     | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:28 |

|   |     |                                                                                                                 |                                             |                  |
|---|-----|-----------------------------------------------------------------------------------------------------------------|---------------------------------------------|------------------|
| - | 107 | ((command\$2) with (generat\$3 near3<br>((software interrupt\$2) or (SWI))) ) and<br>(in\$1circuit emulator\$2) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:29 |
| - | 115 | (command\$2) with ((software adj<br>interrupt\$2) or (SWI))                                                     | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:31 |
| - | 11  | (in\$1circuit emulator\$2) and ((command\$2)<br>with ((software adj interrupt\$2) or (SWI))<br>)                | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>IBM_TDB | 2004/10/26 10:31 |